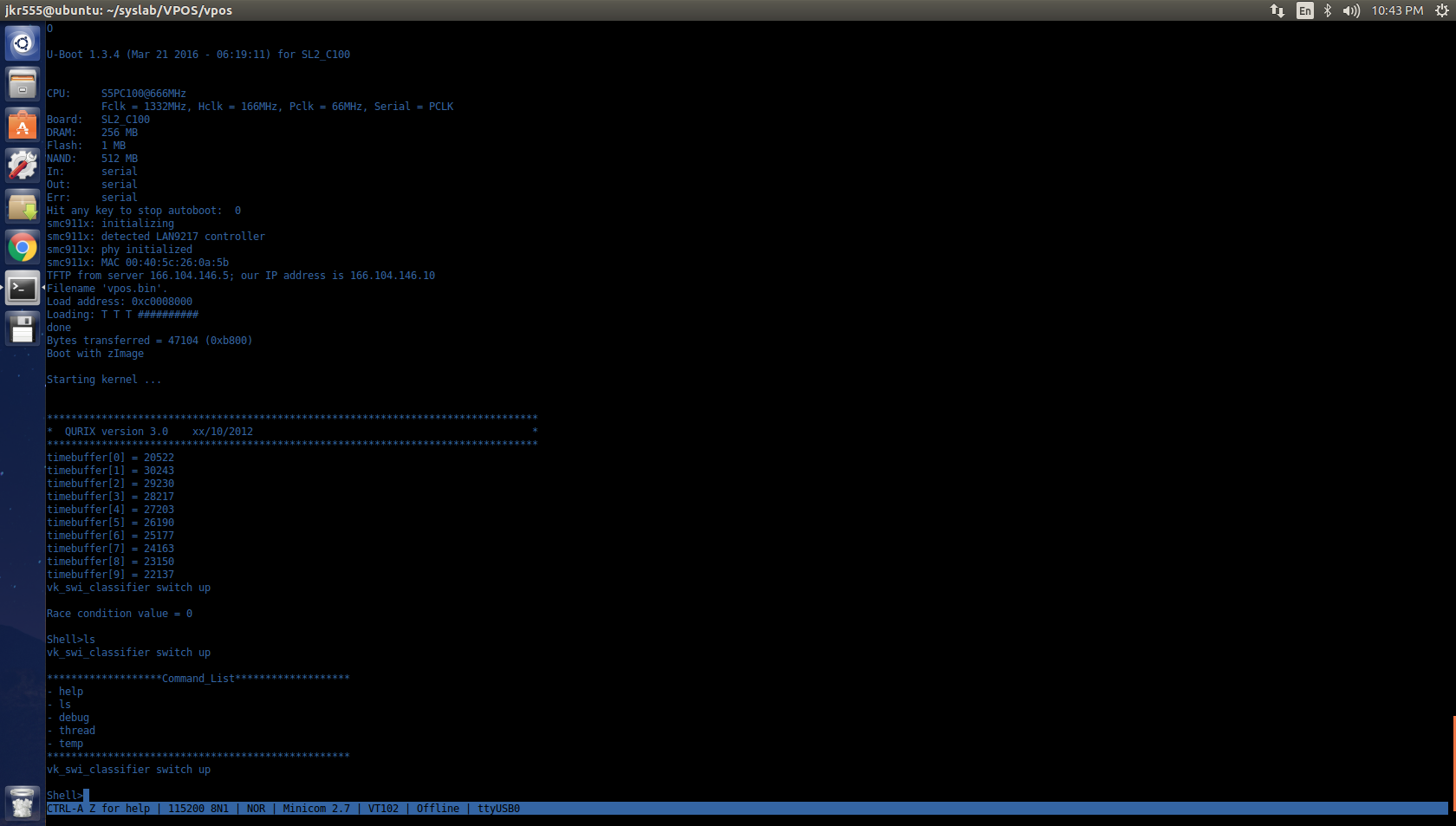
20120011658 김진권

<UART>

1.

vh\_ULCON1 = 0x3;vh\_UCON1 = 0x245;vh\_UFCON1 = 0xc7;vh\_UINTM1 = 0xe;vh\_UINTP1 = 0xf;

2.



<timer>

1.

void vh\_timer\_init(void)

{

//Timer4 Configuration

vh\_TCFG0 = vh\_vTimer\_CFG0;

vh\_TCFG1 = vh\_vTimer\_CFG1;

vh\_TCNTB4 = vh\_vTimer\_TCNTB4;

vk\_timer\_flag = 0;

}

// Reserved [31:24] Dead Zone length [23:16]

// Prescalar1 [15:8] Prescalar0 [7:0]

// Prescalar1 = 132 -> 66,000,000 Hz -> 500,000 Hz

#define vh\_vTimer\_CFG0 0x8300

// Reserved [31:24] Divider MUX4 [19:16]

// Divider MUX3 [15:13] Divider MUX2 [11:8]

// Divider MUX1 [7:4] Divider MUX0 [3:0]

// Divider = 16 -> 500,000 Hz -> 31,250 Hz

#define vh\_vTimer\_CFG1 0x4000

// TCNTB4 = 31,250

#define vh\_vTimer\_TCNTB4 0x7A12

1초에 1번 timer가 발생해야 하므로

presclar & devider 값을 각각 131(132) ,16으로 하여 66,000,000/132/16 = 31,250 Hz하고 TCNTB4 값을 31,250으로 설정한다.

2.

// Pulse Width Modulation Timer bae address

#define vh\_PWM\_BASE 0xEA000000

// Specifies Timer Configuration Register0

#define vh\_TCFG0 (\*(volatile unsigned\*)(vh\_PWM\_BASE+0x00))

// Specifies Timer Configuration Register1

#define vh\_TCFG1 (\*(volatile unsigned\*)(vh\_PWM\_BASE+0x04))

// Specifies Timer Control Register

#define vh\_TCON (\*(volatile unsigned\*)(vh\_PWM\_BASE+0x08))

// Specifies Timer Interrupt Control and Status Register

#define vh\_TINT\_CSTAT (\*(volatile unsigned\*)(vh\_PWM\_BASE+0x44))

// Specifies Timer 4 Count Buffer Register

#define vh\_TCNTB4 (\*(volatile unsigned\*)(vh\_PWM\_BASE+0x3C))

// Specifies Timer 4 Count Observation Register

#define vh\_TCNTO4 (\*(volatile unsigned\*)(vh\_PWM\_BASE+0x40))

3.

